



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | | |
|---------------------------|---|------------------|--------------------|
| Appl. No. | 10/767,419 | Confirmation No. | 4453 |
| First Inventor | Richard K. Williams | Filing Date | 01/29/2004 |
| Tech. Center/ Art Unit | 2822 | Examiner | Mary A. Wilczewski |
| Title: | Modular Bipolar-CMOS-DMOS Analog Integrated Circuit And Power Technology | | |
| Docket No.: | AAT007-2C US | Customer No.: | 34036 |

Santa Clara, California
January 22, 2007

Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

In response to the Office Action dated January 11, 2007, please amend the above-identified application as follows.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Drawings begin on page 3 of this paper and include both attached replacement sheets and annotated sheets showing changes.

Remarks/Arguments begin on page 4 of this paper.

An **Appendix** including amended drawing figures is attached following page 4 of this paper.